APPLICATION BRIEF 24 — HOW THE AUDOUT PIN FUNCTIONS IN THE ISD33000 PRODUCTS

AUDIO OUTPUT (AUD OUT)

This pin provides the audio output to the user. It is capable of driving a 5 KΩ impedance.

The AUD OUT pin is biased up to approximately 1.2 volts unless the ISD33000 is actively recording or the device is in the power-down state. When the device is actively recording or powered down, the pin is in a high-impedance state. This means that there is a transition from high-impedance to 1.2 volts under the following conditions:

- When a SPI cycle is executed to initially set the PU bit and thus power-up the device.
- When a SPI cycle is executed to clear the RUN bit during a RECORD operation, and thus stop recording.
- When the device goes into OVERFLOW during a RECORD operation both ending the recording and setting the OVF interrupt.

There is a transition from 1.2 volts to high-impedance under the following condition:

When a SPI cycle is executed to begin a RECORD operation and in Power-Down Mode.