

BR9080A-W/AF-W/ARFV-W BR9016A-W/AF-W/ARFV-W

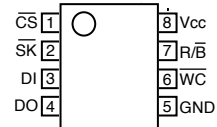
Features

- 8k bit serial EEPROM organized as 512×16 bit (BR9080A)
16k bit serial EEPROM organized as 1024×16 bit (BR9016A)
- Low operating voltage
Read : 2.7~5.5V
Write : 2.7~5.5V
- Low current consumption
Active : 3mA MAX ($V_{cc}=3V$), 5mA MAX ($V_{cc}=5V$)
Standby : 2 μ A MAX ($V_{cc}=3V$), 3 μ A MAX ($V_{cc}=5V$)
- Clock frequency : 2MHz MAX ($V_{cc}=3V, 5V$)
- Write cycle time : 10ms MAX ($V_{cc}=3V, 5V$)
- Address auto-increment function during read operation
- Automatic erase-before-write function during write operation
- Prevent inadvertent writing
Defaults to power up with write-disabled state
Software instructions for write-enable/disable
Write inhibit at low V_{cc}
WC pin to write protection
- READY/BUSY Status indicator function (R/\bar{B} pin, DO pin)
- Schmitt trigger circuit & noise filter are built into \bar{CS} , \bar{SK} , DI pin
- 100,000 write cycle typical
- 10 years data retention
- Operating temperature range: -40~85°C

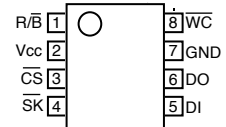
Pin Configurations

BR9080A-W/ARFV-W
BR9016A-W/ARFV-W

BR9080AF-W
BR9016AF-W



DIP8/SSOP-B8

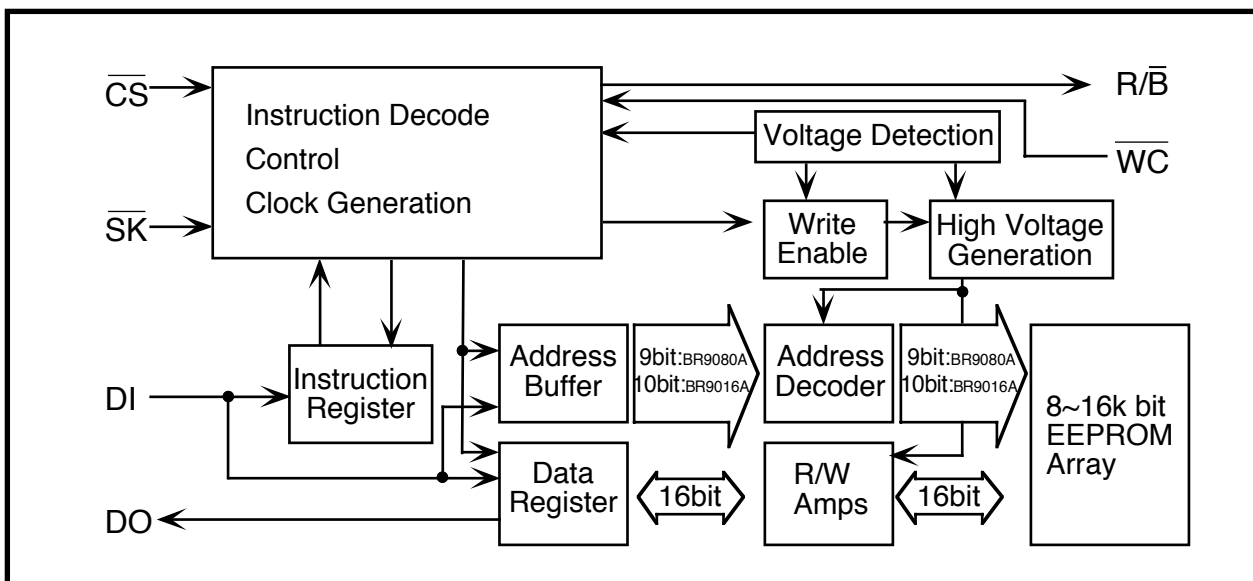


SOP8

Pin Functions

Pin Names	Functions
\bar{CS}	Chip Select
\bar{SK}	Serial Data Clock
DI	Serial Data Input
DO	Serial Data Output
GND	Ground
\bar{WC}	Write Control Input
R/\bar{B}	READY/BUSY Status Signal Output
V_{cc}	Power Supply

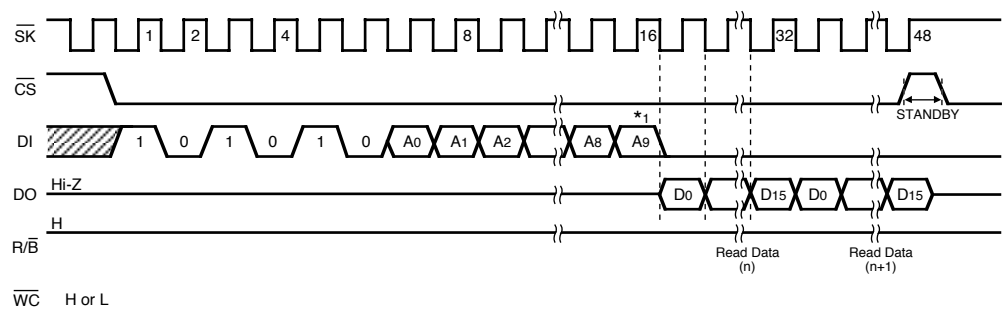
Block Diagram



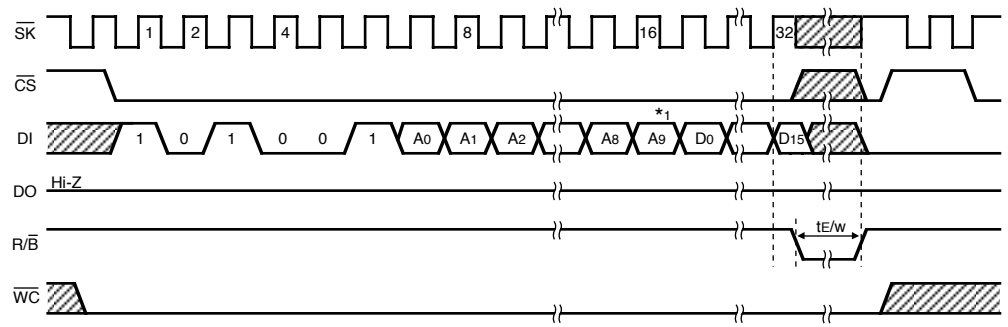
Serial 3 Wire (Direct Connection Serial Port Type)

Timing chart

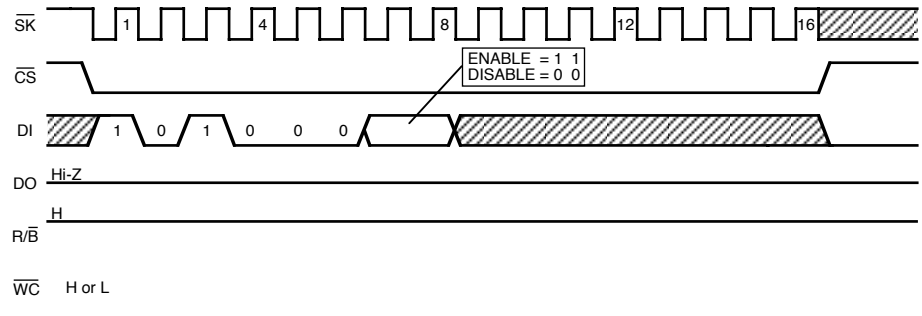
Read cycle



Write cycle



Writing enable / disabled cycle



*1 : Address ; 9bits (BR9080A)
The address should be inputted at the eighth \overline{SK} .

Note : Double-cell type (BR9080/F/RFV, BR9080A-W/AF-W/ARFV-W).
Single-cell types (BR9016/F/RFV).
"-W" means double-cell type.