TOSHIBA BIPOLAR DIGITAL IC SILICON MONOLITHIC

# TD62930P,TD62930F

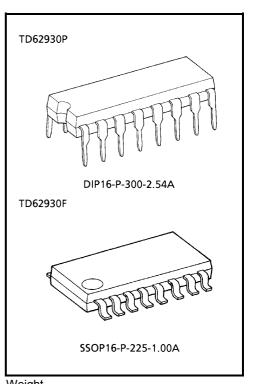
### THREE-CHANNEL SMALL-SIGNAL IGBT GATE DRIVER

The TD62930P and TD62930F are drivers using 5 V-signal input to output the signals required to drive IGBT gates. TD62930P / F is the most suitable for low-side drive of a miniature IGBT to use for inverter for the household electric appliances mainly.

The outputs are separated into high-side and low-side outputs. This separation simplifies the IGBT gate on / off timing control. Two output signals are assigned for one input signal. The high-side output is high-level for high-level input, and high impedance for low-level input. The low-side output is high impedance for high-level input, and low-level for low-level input.

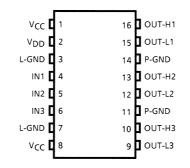
### FEATURES

- Power supply voltage (maximum rating) High-voltage block power supply voltage VCC = 30 V Low-voltage block power supply voltage VDD = 7 V
- Output current (maximum rating) High-side peak current IOUT = -0.4 A (max) Low-side peak current IOUT = 0.4 A (max)
- Input–output response speed tpHL, tpLH ≤ 1 µs (max)
- Package : DIP16 / SSOP16 (1.00 mm pitch)

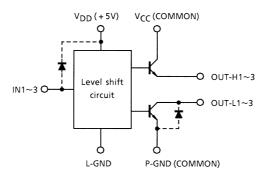


Weight DIP16-P-300-2.54A : 1.11 g (Typ.) SSOP16-P-225-1.00A : 0.14 g (Typ.)

### PIN ASSIGNMENT (TOP VIEW)



### INTERNAL EQUIVALENT CIRCUIT



#### **PIN DESCRIPTION**

PIN No.	PIN NAME	FUNCTION
1, 8	V <sub>CC</sub>	30 V supply pins
2	V <sub>DD</sub>	5 V supply pin
3, 7	L-GND	Ground pins for 5 V supply
4, 5, 6	IN1~3	Input pins for 5 V output control signals
11, 14	P-GND	Ground pins for 30 V supply
9, 12, 15	OUT-L1~3	Low-side output pins
10, 13, 16	OUT-H1~3	High-side output pins

### MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	PIN / PACKAGE	SYMBOL	RATING	UNIT	
Power Supply Voltage	V <sub>CC</sub>	V <sub>CC</sub>	30	V	
Power Supply Voltage	V <sub>DD</sub>	V <sub>DD</sub>	7	V	
Input Voltage	IN1~3	V <sub>IN</sub>	-0.5~V <sub>DD</sub> + 0.5	V	
	OUT-H1~3		0~20 (Ta = −20~85°C)	v	
Output \/altaga	001-H1~3	V <sub>OUT (H)</sub>	0~30 (Ta = −20~70°C)	v	
Output Voltage		N	−0.5~20 (Ta = −20~85°C)	V	
	OUT-L1~3	V <sub>OUT (L)</sub>	−0.5~30 (Ta = −20~70°C)		
High-level Output Peak Current	OUT-H1~3	IOPH (Note 1)	-0.4	A / ch	
Low-level Output Peak Current	OUT-L1~3	IOPL (Note 1)	+0.4	A / ch	
Operating Frequency	IN1~3	f	25	kHz	
Deven Die ein etien	DIP16	P <sub>D1</sub> (Note 2)	1.47 (FREE AIR)	W	
Power Dissipation	SSOP16	P <sub>D2</sub> (Note 2)	0.78 (ON PCB)	W	
Operating Ambient Temperature		T <sub>opr</sub>	-20~85	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C	

Note 1: Output pin current

The pulse width of the output pin current at peak is  $\leq$  1 µs, 300 pps.

Note 2: When ambient temperature exceeds 25°C

Derate the power dissipation of DIP-type devices at 11.76 mW / 1°C (device only) and Derate the power dissipation of SMD-type devices at 6.24 mW / 1°C (mounted on the board).

#### **RECOMMENDED OPERATING CONDITIONS** (Unless otherwise specified, Ta = -20 to $70^{\circ}C$ )

CHARACTERISTIC		PIN	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
line with Violtenne	High level	IN1~3	V <sub>IH</sub>	V <sub>CC</sub> = 15 V, V <sub>DD</sub> = 4.5~5.5 V	3.5	_	—	v
Input Voltage	Low level		V <sub>IL</sub>		_	_	1.0	
Input Current	High level	IN1~3	IIH	V <sub>CC</sub> = 15 V, V <sub>DD</sub> = 4.5~5.5 V	_	_	5	mA
	Low level	- 111~3	١ <sub>IL</sub>		_	_	-5	
Input Power Supply Voltage		V <sub>CC</sub>	V <sub>CC</sub>		10	15	25	v
		V <sub>DD</sub>	V <sub>DD</sub>		4.5	5.0	5.5	
Output Current		OUT-H1~3	IOH (DC)	V <sub>CC</sub> = 20 V, V <sub>DD</sub> = 4.5 V	_	_	-0.1	A
		001-11~3	IOH (Peak)		_	_	-0.35	
		OUT-L1~3	IOL (DC)	V <sub>CC</sub> = 20 V, V <sub>DD</sub> = 4.5 V	_	_	0.1	
		001-11~3	IOL (Peak)		_	_	0.35	
Operating Temperature			T <sub>opr</sub>	V <sub>CC</sub> = 30 V, V <sub>DD</sub> = 5.5 V	-20	25	70	°C
				$V_{CC}$ = 20 V, $V_{DD}$ = 5.5 V	-20	25	85	

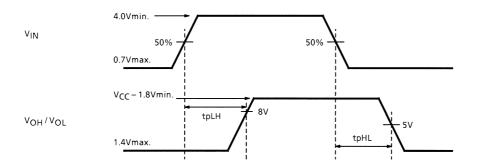
### ELECTRICAL CHARACTERISTICS (Unless otherwise specified, Ta = -20 to $70^{\circ}$ C)

CHARACTERISTIC		PIN	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Input Current	High level	IN1~3	IIH	V <sub>CC</sub> = 15 V, V <sub>IN</sub> = 5 V	0.1	0.2	0.4	mA
	Low level		ار	V <sub>CC</sub> = 15 V, V <sub>IN</sub> = 0 V		0		
Output	High level	OUT-H1~3	V <sub>OH</sub>		VCC -4.0	VCC -1.9	VCC -1.0	V
Voltage	Low level	OUT-L1~3	V <sub>OL</sub>	$\label{eq:VCC} \begin{array}{l} V_{CC} = 15 \; V, \; V_{IL} = 0 \; V, \\ R_{LL} = 100 \; \Omega \end{array}$	0.3	0.5	2.5	
Dissipation Current 1		V <sub>DD</sub>	I <sub>DDL</sub>	V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 0 V, Ta = 25°C		1.5	3.0	mA
				V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 0 V, Ta = −20~85°C	_	_	3.5	
			IDDH	V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 5 V, Ta = 25°C	_	1.8	3.5	
				V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 5 V, Ta = −20~85°C	_	_	4.0	
Dissipation Current 2		Vcc	ICCL	V <sub>CC</sub> = 30 V, V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 0 V, Ta = 25°C	_	10.2	15.0	
				V <sub>CC</sub> = 30 V, V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 0 V	_	_	18.0	
			Іссн	V <sub>CC</sub> = 30 V, V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 5 V, Ta = 25°C	_	7.5	11.0	
				V <sub>CC</sub> = 30 V, V <sub>DD</sub> = 5.5 V, V <sub>IH</sub> = 5 V	_	_	14.0	
Operating Power Supply Voltage		V <sub>CC</sub>	V <sub>CCopr</sub>		10	_	30	V

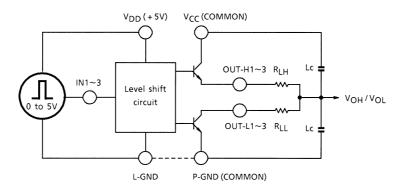
### SWITCHING CHARACTERISTICS (Unless otherwise specified, Ta = -20~70°C)

CHARACTERISTIC		PIN	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Propagation Delay Time	High level	OUT-H1~3	tPLH	$V_{DD} = 5.0 \text{ V}, V_{CC} = 15 \text{ V}$ $R_{LH} = R_{LL} = 100 \Omega$ , $V_{IN} = 0.7 \text{ to } 4 \text{ V}$	_	0.25	1.00		
	Low level	OUT-L1~3	tPHL		_	0.25	1.00	μs	

### SWITCHING WAVEFORM



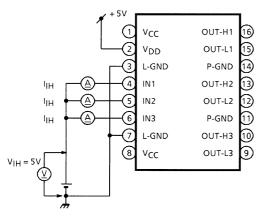
### **PROPAGATION DELAY TIME TEST CIRCUIT**



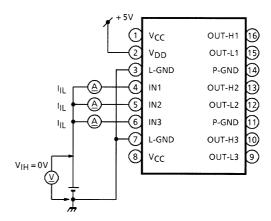
Toshiba recommends connecting load resistors as in the above diagram, utilizing the independence of the high-level and low-level sides of this IC.

### **TEST CIRCUIT**

(1) I<sub>IH</sub>

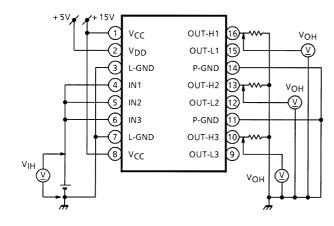


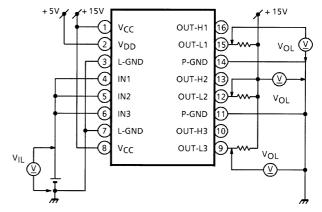
(2) I<sub>IL</sub>



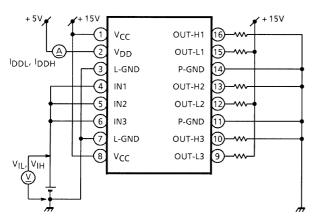
### (3) $V_{IH}, V_{OH}$



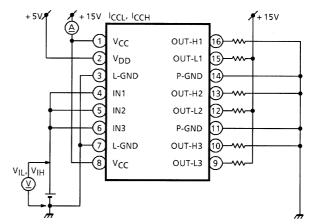




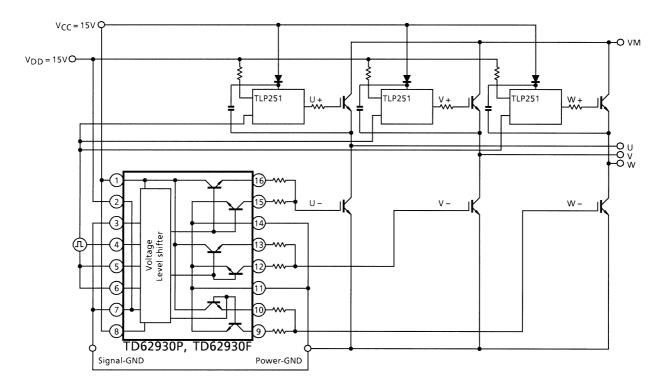
### (5) I<sub>DDL</sub>, I<sub>DDH</sub>



(6) ICCL, ICCH



### **APPLICATION CIRCUIT**



#### **PRECAUTIONS for USING**

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors.

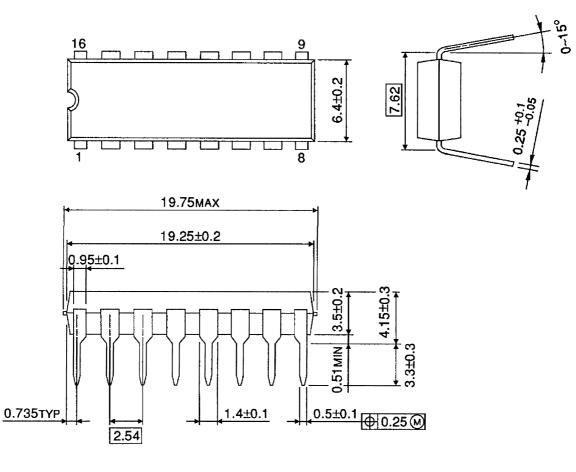
Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line, V<sub>CC</sub> and GND (L–GND, P–GND) line since IC may be destroyed due to short–circuit between outputs, air contamination fault, or fault by improper grounding.

### PACKAGE DIMENSIONS

DIP16-P-300-2.54A

Unit: mm



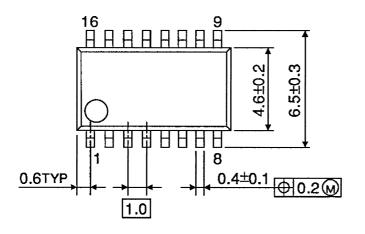
Weight: 1.11 g (Typ.)

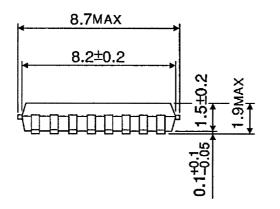
### TD62930P/F

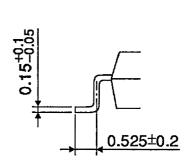
# PACKAGE DIMENSIONS

SSOP16-P-225-1.00A

Unit: mm







5.72 (225mil)

Weight: 0.14 g (Typ.)

#### **RESTRICTIONS ON PRODUCT USE**

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