TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TD62786AP,TD62786F,TD62786AF TD62787AP,TD62787F,TD62787AF

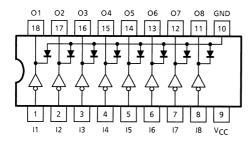
### 8CH HIGH-VOLTAGE SOURCE DRIVER

The TD62786AP / F / AF series are eight channel huyx non-inverting source current transistor array. All units feature integral clamp diodes for switching inductive loads. Applications include relay, hammer and lamp drivers.

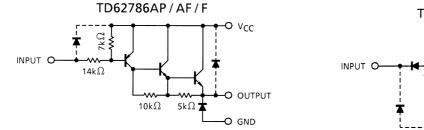
### FEATURES

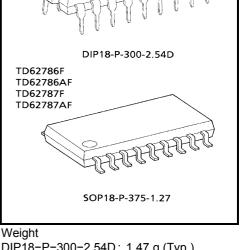
- High output voltage type-AP, AF : VCE (SUS) = 50 V (Min) type-F : VCE (SUS) = 35 V (Min)
- Output current (single output)
- : IOUT = -500 mA / ch (Max)
- Output clamp diodes
- Single supply voltage
- Input compatible with TTL, 5 V CMOS
- Low level active input
- Package type-AP : DIP-18 pin
- Package type-F, AF: SOP-18 pin

### **PIN CONNECTION (TOP VIEW)**



## SCHEMATICS (EACH DRIVER)

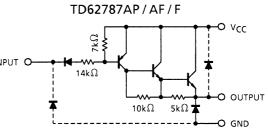




TD62786AP

TD62787AP

DIP18-P-300-2.54D: 1.47 g (Typ.) SOP18-P-375-1.27 : 0.41 g (Typ.)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

## MAXIMUM RATINGS (Ta = 25°C)

CHARACTER	RISTIC	SYMBOL	RATING	UNIT	
Supply Voltage	AP / AF		50	v	
	F	V <sub>CC</sub> -V <sub>GND</sub>	35	v	
Output Sustaining Voltage	AP / AF	V <sub>OUT</sub>	-50	V	
	F	V001	-35	v	
Output Current		IOUT	-500	mA / ch	
Input Voltage		V <sub>IN</sub> (Note 1)	-30~0.5	V	
Input Voltage		V <sub>IN</sub> (Note 2)	V <sub>GND</sub> ~7	V	
Clamp Diode Forward Current	AP / AF	V <sub>R</sub>	50	v	
	F	٧R	35	v	
Clamp Diode Forward	Current	١ <sub>F</sub>	500	mA	
Power Dissipation	AP	P <sub>D</sub> (Note 3)	1.47	W	
	F / AF		0.96	vv	
Operating Temperature	9	T <sub>opr</sub>	-40~85	°C	
Storage Temperature		T <sub>stg</sub>	-55~150	°C	

Note 1: Only TD62786AP / F / AF

Note 2: Only TD62787AP / F / AF

Note 3: Delated above 25°C in the proportion of 11.7 mW / °C (AP Type), 7.7 mW / °C (F, AF Type).

### **RECOMMENDED OPERATING CONDITIONS (Ta = -40 \sim 85^{\circ}C, V<sub>CC</sub> = 0 V)**

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage	AP / AF	V <sub>CC</sub> -V <sub>GND</sub>	_		_	50	v
	F		_	_	_	35	
Output Voltage	AP / AF	V <sub>OUT</sub>	_	_	_	-50	v
	F		_	_	_	-35	
Output Current		IOUT	_		_	-350	mA / ch
Input Voltage	TD62786	V <sub>IN</sub>	_	-30	_	0	v
	TD62787		_	$V_{GND}$	_	7	v
Clamp Diode Reverse Voltage	AP / AF	V <sub>R</sub>	_	_	_	50	V
	F		_	_	_	35	
Clamp Diode Forward Current		١ <sub>F</sub>	_	_	_	350	mA
Power Dissipation	AP	P <sub>D</sub>	_	_		0.52	w
	AF / F		—	—	—	0.35	٧V

# ELECTRICAL CHARACTERISTICS (Ta = $25^{\circ}$ C, V<sub>CC</sub> = 0 V)

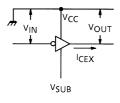
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Output Leakage Current		ICEX	1	V <sub>OUT</sub> = V <sub>GND</sub> = −50 V Ta = 85°C	_		-100	μA	
Output Saturation Voltage		V <sub>CE (sat)</sub>	2	V <sub>IN</sub> = V <sub>IL MAX.</sub> I <sub>OUT</sub> = -100 mA	-	-	-1.8	v	
				V <sub>IN</sub> = V <sub>IL MAX.</sub> I <sub>OUT</sub> = −350 mA	_	_	-2.0		
DC Current transfer Ratio		h <sub>FE</sub>	2	V <sub>CC</sub> = 0 V, V <sub>CE</sub> = 3 V I <sub>OUT</sub> = -350 mA	1000	_	_	_	
Input Voltage	"H" Level	TD62786	VIN	4	_	-1.2		0	V
		TD62787				-1.6		5.5	
	"L" Level	TD62786				-30		-2.8	
		TD62787				V <sub>GND</sub>		-3.7	
Input Current		۱ <sub>IL</sub>	_	V <sub>CC</sub> = 5.5 V, V <sub>IN</sub> = 0.4 V		_	-0.4	mA	
Clamp Diode Reverse Current		I <sub>R</sub> ·	_	V <sub>R</sub> = V <sub>R MAX.</sub> , Ta = 85°C		_	100	μΑ	
					_	_	100		
Clamp Diode Forward Voltage		V <sub>F</sub>	_	—	_	_	2.0	V	
Turn-On Delay		t <sub>ON</sub>	- 5	V <sub>OUT</sub> = −50 V, R <sub>L</sub> = 163 Ω C <sub>L</sub> = 15 pF (Note)	_	0.2	_	μs	
Turn Off Delay		t <sub>OFF</sub>	5		—	1.0	—	μs	

Note:  $V_{OUT}$  = -35 V, R<sub>L</sub> = 116  $\Omega$  for Type-F

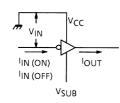
# <u>TOSHIBA</u>

# **TEST CIRCUIT**

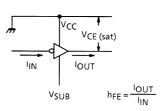
1. ICEX



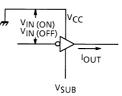
3. I<sub>IN (ON)</sub>, I<sub>IN (OFF)</sub>



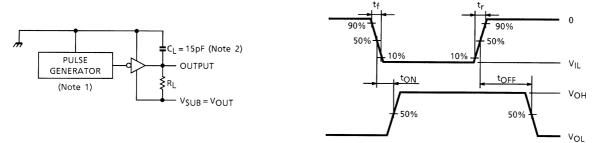
## 2. V<sub>CE (sat)</sub>, h<sub>FE</sub>



4. V<sub>IN (ON)</sub>, V<sub>IN (OFF)</sub>







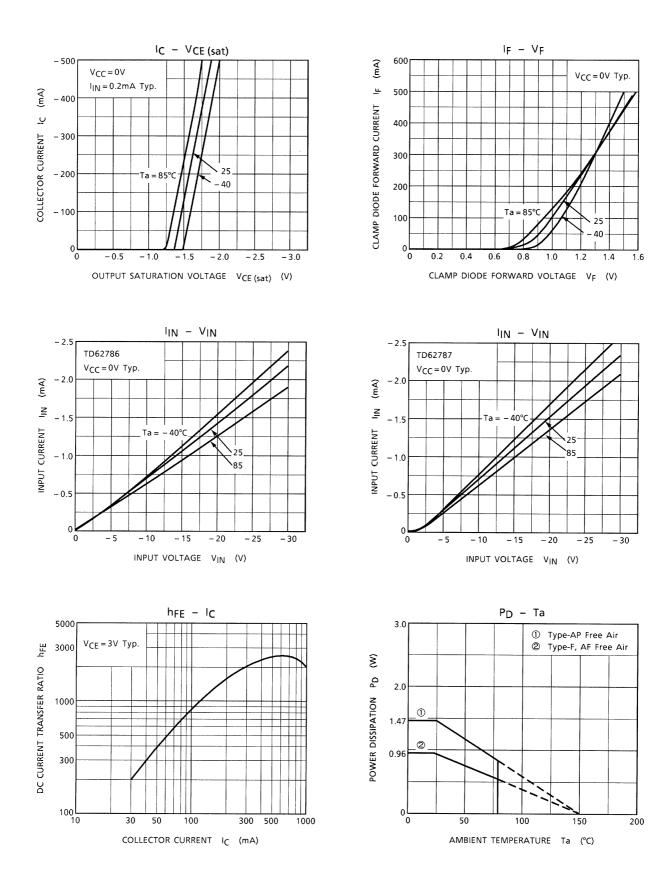
Note 1: Pulse Width 50  $\mu$ s, Duty Cycle 10% Output Impedance 50  $\Omega$ , t<sub>f</sub> ≤ 10 ns, t<sub>f</sub> ≤ 5 ns Note 2: C<sub>L</sub> includes probe and jig capacitance.

# **PRECAUTIONS for USING**

This IC does not integrate protection circuits such as overcurrent and overvoltage protectors. Thus, if excess current or voltage is applied to the IC, the IC may be damaged. Please design the IC so that excess current or voltage will not be applied to the IC.

Utmost care is necessary in the design of the output line,  $V_{CC}$  and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

# **TOSHIBA**

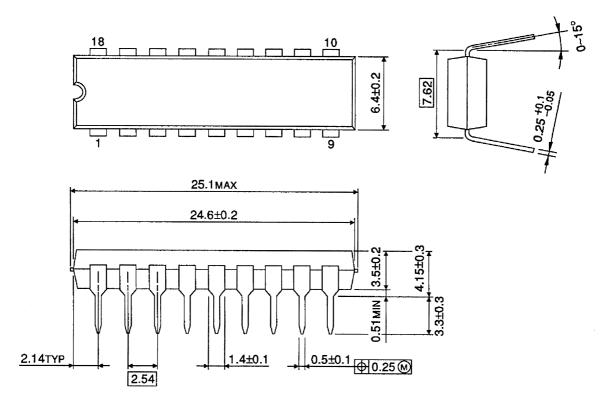


# TOSHIBA

#### PACKAGE DIMENSIONS

DIP18-P-300-2.54D

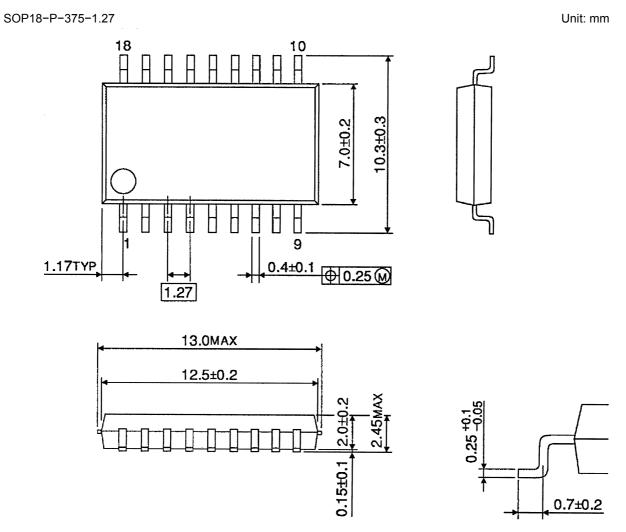
Unit: mm



Weight: 1.47 g (Typ.)

# **TOSHIBA**

#### PACKAGE DIMENSIONS



Weight: 0.41 g (Typ.)

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